

## AMENDMENTS

### IN THE ABSTRACT:

A low drop-out DC voltage regulator for regulating a voltage from a DC power supply ( $V_{\text{supply}}$ ) applied to a load (3) at an output of the regulator and comprising a pass device (T7) for controlling flow of current from the power supply to the load so as to control the output voltage ( $V_{\text{out}}$ ) at the regulator output, and a feedback loop for controlling the pass device (T7). The feedback loop comprises a resistive feedback path (5) and a capacitive feedback path that includes a feedback capacitive element (6) in series, and comparator means responsive to signals from the feedback paths for applying to the pass device (T7) an error signal that is a function of the value of the output voltage ( $V_{\text{out}}$ ) relative to a nominal value so as to control the output voltage ( $V_{\text{out}}$ ). The comparator means comprises feedback current producing means (8-10) for maintaining a common point (7) of the resistive feedback path (5) and the capacitive feedback path (6) at a reference voltage ( $V_{\text{ref}}$ ) so as to produce a feedback current flowing in the resistive feedback path (5) and in the capacitive feedback path (6) in parallel between the regulator output and the common point (7), and current comparison means (10, 11, 15) responsive to relative values of the feedback current and of a reference current ( $V_{\text{ref}}/R1$ ) for producing the error signal.

Figure-5